

# DECODER-USABLE SYNDROME GENERATION WITH REPRESENTATION GENERATED WITH INFORMATION BASED ON VECTOR PORTION

5

## FIELD OF THE INVENTION

The present invention relates generally to decoders and more particularly to a syndrome usable in a decoder.

## BACKGROUND OF THE INVENTION

10

As will be understood by those skilled in the art, a syndrome comprises a basic element of a decoding procedure. In one example, the syndrome can be used to identify the bits in error.

15

One example of a decoder comprises a Bose, Chaudhuri, and Hocquenghem ("BCH") decoder. A BCH decoder operates with BCH codes. BCH codes constitute a broad class of error-correcting and error-detecting codes. BCH codes are typically designed using the theory developed for Galois field ("GF") arithmetic. Examples of BCH codes include binary BCH codes and Reed-Solomon codes. Hamming codes, for example, comprise a subset of binary BCH codes. BCH codes comprise a subset of cyclic codes, as will be understood by those skilled in the art.

20

A binary BCH code, in one example, is characterized by a block length  $N$ , an information vector length  $K$ , a number of bits per symbol  $m$  where  $N = 2^m - 1$ , a primitive element  $\alpha$  used to construct a Galois field  $GF(2^m)$ , a generator polynomial  $g(x)$ , and a guaranteed error correction capability  $t$ . Commonly, those skilled in the art interchangeably employ the terms "vector" and "polynomial." For example, the  $K$ -bit information vector  $u[0], \dots, u[K-1]$  is represented as the polynomial

25

09524172-0313000

$u(x) = u[K-1]x^{K-1} + u[K-2]x^{K-2} + \dots + u[1]x + u[0]$ , where the coefficients  $u[0]$ , ..., and  $u[K-1]$  are elements of  $GF(2^m)$ , and are binary in the case of binary BCH codes.

A detailed example is now presented for explanatory purposes. A  $K$ -bit information vector  $u(x)$  is encoded into an  $N$ -bit codeword  $v(x)$  by polynomial multiplication with  $g(x)$ . In one example, the encoder is initialized to some value, such as zero, before the polynomial multiplication. This codeword is transmitted, possibly corrupted by the channel, and received as a vector  $r(x)$ . A BCH decoder estimates a decoded information vector  $w(x)$  based on  $r(x)$  and possibly additional information.

Several decoder techniques have been developed to estimate  $w(x)$  from  $r(x)$ . In one example of a BCH decoder, syndromes are computed by evaluating the received vector  $r(x)$  at consecutive roots of the generator polynomial  $g(x)$ . The syndromes characterize the difference from the received vector to the nearest codeword. So, the syndromes can be used in a decoder to correct this difference.

The properties of a generator polynomial, in one example, can suggest a design of a BCH decoder. One can assume, for example, that the necessary roots of the generator polynomial comprise at least  $2t$  consecutive values  $\alpha^{L+1}, \alpha^{L+2}, \dots, \alpha^{L+2t}$ , where  $\alpha$  represents the primitive element used to construct the Galois field  $GF(2^m)$ , and  $L$  represents a starting power of the roots. In one example, when the roots comprise  $\alpha^1, \alpha^2, \dots, \alpha^{2t}$  ( $L=0$ ), it is understood by those skilled in the art that the syndromes  $s_j$  for binary BCH codes possess a property as described by the following exemplary Equation (1).

$$s_{2j} = (s_j)^2 \quad j = 1, \dots, t \quad (1)$$

Because of Equation (1), in one example, the odd-numbered syndromes can be first computed by evaluating the received vector  $r(x)$  at the necessary odd-powered roots of the generator polynomial. The even-numbered syndromes can then be computed by using Equation (1). In this example, the number of operations in a BCH decoder can be reduced.

In another example, when the necessary roots of the generator polynomial are consecutive, the first root and error-correcting capability  $t$  comprise sufficient information to determine the subsequent roots of the generator polynomial. In this example, a BCH decoder can reduce the amount of information (e.g., memory usage) needed.

Computing syndromes typically comprises a computationally-intensive task because the computation typically involves polynomial evaluation using Galois field arithmetic. To compute syndrome  $s_j$  directly, the received vector  $r(x)$  is evaluated at root  $\alpha^{j+L}$ , as follows in exemplary Equation (2).

$$s_j = r(\alpha^{j+L}) = r_{N-1}(\alpha^{j+L})^{N-1} + r_{N-2}(\alpha^{j+L})^{N-2} + \dots + r_1(\alpha^{j+L}) + r_0 \quad (2)$$

Evaluating Equation (2), in one example, requires  $N-1$   $\text{GF}(2^m)$  additions,  $N-1$  general  $\text{GF}(2^m)$  multiplications, and  $N-1$  general  $\text{GF}(2^m)$  exponentiations per syndrome.

When representing the Galois field elements as an  $m$ -tuple over a standard canonical basis  $(\alpha^{m-1}, \dots, \alpha, 1)$ ,  $\text{GF}(2^m)$  addition is equivalent to a simple exclusive-OR operation. Further,  $\text{GF}(2)$  multiplication is equivalent to a logical AND operation for  $m = 1$ . However, the general  $\text{GF}(2^m)$  multiplications and general  $\text{GF}(2^m)$  exponentiations typically have no simple implementation for  $m > 1$  in the standard canonical basis. To minimize the number of these exponentiations, one technique

for evaluation of polynomials employs an iterative algorithm, such as Horner's rule. For instance, Equation (2) can be expressed as follows in exemplary Equation (3).

$$s_j = r(\alpha^{j+L}) = r(\beta) = (\dots((r_{N-1}\beta + r_{N-2})\beta + r_{N-3})\beta + \dots + r_1)\beta + r_0 \text{ where } \beta = \alpha^{j+L} \quad (3)$$

On the one hand, Equation (3) eliminates the general  $GF(2^m)$  exponentiations. On the other hand, Equation (3) nevertheless as a shortcoming requires  $N-1$  general  $GF(2^m)$  multiplications per syndrome.

To address the shortcoming of requiring  $GF(2^m)$  multiplications in syndrome calculation, a number of hardware and software approaches have been offered. For example, certain hardware designs implement a general  $GF(2^m)$  multiplier. One shortcoming of a typical hardware implementation of a general  $GF(2^m)$  multiplier is the relatively large amount of power consumption required by the general  $GF(2^m)$  multiplier.

Further, a number of software implementation that use lookup tables have been offered to perform general  $GF(2^m)$  multiplication for syndrome calculations. In one example, the multiplicands are transformed from the standard canonical basis into an exponential representation by using lookup tables. In the exponential representation, integer arithmetic (i.e., addition, modulo) replaces  $GF(2^m)$  multiplication. Once the integer result has been computed, a lookup table is used to transform the integer result into the standard canonical basis. For instance, if  $a$  and  $b$  are elements of  $GF(2^m)$ , the steps of a general  $GF(2^m)$  multiplier in one software implementation for computing  $c = a \times b$  are as follows.

```

if ((a == 0) or (b == 0))
    c = 0
else
    a' = LUT(a)
    b' = LUT(b)
    c' = (a' + b') mod (2m - 1)
    c = LUT-1(c')

```

In the above steps, LUT is the lookup table used to transform a number from the standard canonical basis into an exponential representation, and LUT<sup>-1</sup> is the lookup table that transforms an exponential representation into the standard canonical basis.

This exemplary software implementation suffers from the use of lookup tables. As one shortcoming, the memory requirement of the lookup tables is proportional to 2<sup>m</sup>. For modest *m*, such as *m*=9, the memory requirement therefore undesirably exceeds 512 words. As another shortcoming, the required accessing of the lookup tables limits the processing capabilities of the software implementation. This use of the lookup table undesirably lowers the data rate that is sustainable for a software implementation.

Thus, a need exists for enhanced generation of a syndrome that is usable in a decoder.

### **BRIEF DESCRIPTION OF THE DRAWINGS**

FIG. 1 is a functional block diagram of one example of a communications system that encodes and decodes a vector.

FIG. 2 is a functional block diagram that depicts exemplary details of a decoder component of the communications system of FIG. 1:

FIG. 3 is a functional block diagram that depicts illustrative details of one example of a syndrome generator component of the decoder component of FIG. 2.

FIG. 4 is a functional block diagram of exemplary details of a reducer component of the syndrome generator component of FIG. 3.

FIG. 5 is a functional block diagram of exemplary details of a linear feedback shift register element component of the reducer component of FIG. 4.

FIG. 6 depicts one example of logic employed by a converter component of the syndrome generator component of FIG. 3.

FIG. 7 depicts exemplary logic employed by one example of a syndrome computer component of the decoder component of FIG. 2.

0947203100

~~The invention encompasses a method for generating a syndrome usable in a~~  
decoder. There is employed information, that is based on a portion of a vector, to  
generate a representation. There is generated, with employment of the  
representation, the syndrome.

Another embodiment of the invention encompasses a system for generating a syndrome usable in a decoder. A reducer employs information, that is based on a portion of a vector, to generate a representation. A converter generates, with employment of the representation, the syndrome.

A further embodiment of the invention encompasses an article of manufacture. At least one computer usable medium has computer readable program code means embodied therein for causing generation of a syndrome usable in a decoder. There is provided computer readable program code means for causing a computer to employ information, that is based on a portion of a vector, to generate a representation. There is also provided computer readable program code means for causing a computer to generate, with employment of the representation, the syndrome.

In one advantageous aspect, the invention can enable lowered-complexity correction of a number of errors, for example, bit errors. For instance, this can serve to desirably increase life of a battery. In one example, the invention serves to reduce the overall complexity of error correction by advantageously reducing the complexity of syndrome calculation.

A detailed discussion of an exemplary embodiment of the invention is presented herein, for illustrative purposes.

FIG. 1 illustrates a functional block diagram of one example of a communications system 100. System 100, in one example, includes a plurality of components such as computer software and/or hardware components. For instance, a number of such components can be combined or divided, as will be appreciated by those skilled in the art.

Referring still to FIG. 1, in one example of system 100, an encoder 110 is initialized with an initialization 118. The encoder 110, for instance, multiplies an information vector  $u(x)$  112 by a generator polynomial  $g(x)$  114 to produce a codeword vector  $v(x)$  116. In one example, the necessary roots of the generator polynomial  $g(x)$  114 comprise at least  $2t$  consecutive values. For example,  $t$  comprises a guaranteed error correction capability of a binary Bose, Chaudhuri, and Hocquenghem ("BCH") code. The codeword vector  $v(x)$  116 is transmitted through a channel 120. In one example, the channel 120 possibly corrupts the codeword vector  $v(x)$  116. The output of the channel 120 is a received vector  $r(x)$  122. A decoder 130 processes the received vector  $r(x)$  122 by using the generator polynomial  $g(x)$  114 to produce a decoded vector  $w(x)$  132. The decoded vector  $w(x)$  132 comprises an estimate of the information vector  $u(x)$  112 by the decoder 130. One example of decoder 130 comprises (e.g., computer) processor 602 coupled with memory 604. Memory 604, in one example, serves to store logic, for instance, a software implementation.

FIG. 2 illustrates exemplary details of the decoder 130 of the communications system 100 (FIG. 1). In one example, a roots ROM 210 comprises the necessary roots of the generator polynomial  $g(x)$  114 and produces a roots array  $\alpha$  215. In one example, the necessary roots can be computed from the polynomial  $g(x)$  114. In another example, the roots of the polynomial  $g(x)$  114 can be precomputed by the



sub  
al

W.P. Wild  
uses

10

16

FIG. 3 illustrates exemplary details of the syndrome generator 220 of the decoder 130 (FIGS. 1-2). In one example, a reducer 340 processes the received vector  $r(x)$  122 using the initialization 118 and a minimal polynomial  $p_f(x)$  352 for producing a representation  $c_f(x)$  342. The minimal polynomial  $p_f(x)$  352 corresponds

to the generator polynomial root 212. For instance, a reduction mask ROM 350 comprises the minimal polynomial  $p_j(x)$  352. In a further example, the reduction mask ROM 350 also comprises a minimal polynomial degree  $k_j$  354 corresponding to the minimal polynomial  $p_j(x)$  352. In one example, the minimal polynomial  $p_j(x)$  352 and the minimal polynomial degree  $k_j$  354 can be precomputed because the decoder 130 knows the generator polynomial  $g(x)$  114 (FIGS. 1-2). A detailed discussion of an exemplary procedure for performing such a computation is presented herein. In one example, reduction mask ROM 350 comprises one or more reduction masks 351. For instance, syndrome generator 220 generates a reduction mask 351 from a generator polynomial root 212, and employs the reduction mask 351 to generate representation  $c_j(x)$  342.

Again referring to FIG. 3, a conversion mask ROM 320 of syndrome generator 220 comprises one or more instances of conversion mask  $d_j(x)$  322 corresponding to the generator polynomial root 212 and the minimal polynomial degree  $k_j$  354. For example, the one or more conversion masks  $d_j(x)$  322 can be precomputed because the decoder 130 (FIGS. 1-2) knows the generator polynomial  $g(x)$  114 (FIGS. 1-2). In one example, a converter 330 employs (e.g., transforms) the representation  $c_j(x)$  342 using the one or more conversion masks  $d_j(x)$  322 to generate (e.g., produce) the syndrome  $s_j$  222.

FIG. 4 illustrates exemplary details of the reducer 340 of the syndrome generator 220 (FIGS. 2-3). In one example, the reducer 340 comprises a linear feedback shift register ("LFSR") 480 and an indexer 430. For example, the indexer 430 is responsive to the minimal polynomial degree  $k_j$  354 for producing an index  $q_0$  431 set to 0, an index  $q_i$  432 set to  $i-1$ , etc., and an index  $q_{k-1}$  433 set to  $k_j-1$ . The LFSR 480 comprises, for instance, a number of LFSR elements 490. The index  $q_i$

432, in one example, can control the inputs to the LFSR element 490. In one example, the number of LFSR elements 490, is determined by the minimal polynomial degree  $k$ ; 354. LFSR elements 490 produce, for example, outputs  $c_0$  441,  $c_i$  442, etc., and  $c_{k-1}$  443 that comprise the representation  $c_j(x)$  342. In addition,

5 LFSR elements 490 produce symbols  $f_0$  421,  $f_i$  422,  $f_{i+1}$  423, etc., and  $f_{k-1}$  420. In one example,  $f_{k-1}$  420 comprises a feedback symbol for recursion with respect to the LFSR elements 490.

FIG. 5 illustrates exemplary details of an LFSR element 490 of the LFSR 480 (FIG. 4). For instance, a switch 550 selects an output 532, from a GF(2) adder 530,

10 to produce a delay input 552. At reset, the switch 550 selects an extracted initialization 514. A delay 520 holds the delay input 552 from the switch 550, and produces a delay output 522. A switch 560 directs the delay output 522 to the symbol  $f_{i+1}$  423. The switch 560, after processing information (e.g., output 522) that is based on a portion of a received vector  $r(x)$  122 (FIGS. 2-4), directs the delay

15 output 522 to the output  $c_i$  442.

Further referring to FIG. 5, an extractor 513 of LFSR element 490, in one example, uses the index  $q_i$  432 to extract the corresponding element of initialization 118, for producing the extracted initialization 514. For instance, an extractor 511 uses the index  $q_i$  432 to extract the corresponding element of the minimal polynomial

20  $p_j(x)$  352, for producing a binary coefficient  $p$  512. For example, a GF(2) multiplier 540 multiplies the binary coefficient  $p$  512 and the feedback symbol  $f_{k-1}$  420 to produce a product symbol 542. The multiplier 540, in one example, operates over GF(2), so its operation advantageously reduces to a logical-AND operation. For instance, GF(2) adder 530 combines the symbol  $f_i$  422 and the product symbol 542,

25 to produce the output 532.

Referring now to FIG. 3, an exemplary procedure for computing the minimal polynomial  $p_j(x)$  352 and its minimal polynomial degree  $k_j$  354 corresponding to the generator polynomial root 212, is discussed. One can assume, for example, that the necessary roots of the generator polynomial  $g(x)$  114 (FIGS. 1-2) comprise the values  $\alpha^{L+1}, \alpha^{L+2}, \dots, \alpha^{L+2t}$ . The assumption of consecutive roots is reasonable because most generator polynomials have consecutive roots. Although the starting power  $L$  can be set to an arbitrary value,  $L$  is often set to 0.

For instance, the minimal polynomial  $p_j(x)$  352 is computed by employing the following exemplary Equations 4-5.

$$p_j(x) = \prod_{i \in CC(j)} (x - \alpha^i) \quad (4)$$

$$CC(j) = \{(j \cdot 2^l) \bmod (2^m - 1), l = 0, 1, 2, \dots, k_j - 1\} \quad (5)$$

In Equations 4-5,  $m$  is the number of bits per symbol,  $\alpha$  is the primitive element used to construct the Galois field  $GF(2^m)$ ,  $k_j$  (the minimal polynomial degree  $k_j$  354) is the degree of the minimal polynomial  $p_j(x)$  352 with  $k_j \leq m$ , and  $CC(j)$  is a cyclotomic coset of  $\alpha_j$ .

In addition, the following illustrative Table 1 lists the minimal polynomial degrees  $k_j$  354 of a given generator polynomial root 212 and a given  $m$  for BCH codes of length  $\leq 2^{13}-1$  that correct up to  $t=7$  errors.

Table 1: Minimal polynomial degrees  $k_j$  354.

$M$	$2^m-1$	$\alpha$	$\alpha^3$	$\alpha^5$	$\alpha^7$	$\alpha^9$	$\alpha^{11}$	$\alpha^{13}$
3	7	3	3	$\alpha^3$	N/A	N/A	N/A	N/A
4	15=3×5	4	4	<u>2</u>	4	$\alpha^3$	$\alpha^7$	$\alpha^7$
5	31	5	5	5	5	$\alpha^5$	5	$\alpha^{11}$
6	63=3×3×7	6	6	6	6	<u>3</u>	6	6
7	127	7	7	7	7	7	7	7

$M$	$2^m-1$	$\alpha$	$\alpha^3$	$\alpha^5$	$\alpha^7$	$\alpha^9$	$\alpha^{11}$	$\alpha^{13}$
8	255=3×5×17	8	8	8	8	8	8	8
9	511=7×73	9	9	9	9	9	9	9
10	1023=3×11×31	10	10	10	10	10	10	10
11	2047=13×89	11	11	11	11	11	11	11
12	4095=3×3×5×7×13	12	12	12	12	12	12	12
13	8191	13	13	13	13	13	13	13

For a number of codes of interest, the minimal polynomial degree  $k_j$  354 equals  $m$  for all syndromes  $s$  225 that need to be computed. If a polynomial root has the same minimal polynomial  $p_j(x)$  352 as another root, the other root is listed instead of the degree. Note that the underlined entries in Table 1 have  $k < m$ :  $\alpha^5$  in  $GF(2^4)$  and  $\alpha^9$  in  $GF(2^6)$ . Also note that the degree of all but the two entries with  $k < m$  and of  $\alpha^9$  in  $GF(2^4)$ , can be computed as follows.

1.  $m$  prime  $\Rightarrow k = m$
2. If  $j$  is the exponent of the element  $\alpha_j$ , when  $\gcd(2^m - 1, j) = 1 \Rightarrow k = m$  ("gcd" represents the greatest common denominator operation).
3. If  $j$  is the exponent of the element  $\alpha_j$ ,  $m > 2 \lfloor \log_2 j \rfloor \Rightarrow k = m$  where

$$\lfloor x \rfloor = \begin{cases} x & x \text{ integer} \\ \text{int}(x+1) & \text{otherwise} \end{cases}$$

For the exceptions, a table of published minimal polynomials can be used, or the cyclotomic cosets can be computed using Equation (5).

In one example, use of minimal polynomial  $p_j(x)$  352 advantageously allows the general  $GF(2^m)$  multiplier employed in previous designs, to be replaced by reducer 340 and the converter 330, as in FIG. 3. The reducer 340 desirably operates in a  $k$ -tuple basis with respect to  $(\alpha^j)^{k-1}, \dots, \alpha^j, 1$ , whereas a general  $GF(2^m)$  multiplier operates in the standard canonical basis  $(\alpha^{m-1}, \dots, \alpha, 1)$ . The converter 330 advantageously transforms the  $k$ -tuple basis into the standard canonical basis. The

$$c_j = r(\alpha') = (\dots((r_{N-1}\alpha' + r_{N-2})\alpha' + r_{N-3})\alpha' + \dots + r_1)\alpha' + r_0 \quad (6)$$

10

15

(7)

20 representation  $c_j(x)$  342, for computing the syndrome  $s_j$  222 such as by the following  
exemplary Equation (8).

(8)

Although Equation (8) may appear to require general GF multiplications, advantageously no general GF( $2^m$ ) multiplication is necessary. Because the

```


$$z = c_0$$

do  $i = 1, \dots, k_j - 1$ 
  
$$z = z \wedge \begin{cases} d_i & \text{if } c_i = 1 \\ 0 & \text{otherwise} \end{cases}$$

end do

$$s_j = z$$


```

Furthermore, a number of other implementations of Equation (8) may be employed, as will be appreciated by those skilled in the art.

FIG. 6 represents one example of logic 600 for producing syndromes  $s_j$  225 (FIG. 2). In one example, converter 330 (FIG. 3) employs (e.g., performs) logic 600. Initialization 610 sets a counter  $i$  to 0. Initialization 615 sets adder  $z$  to 0. STEP 620 fetches the  $i^{\text{th}}$  element of one or more instances of conversion mask  $d_f(x)$  322. In STEP 625, the fetched element from STEP 620, is added (in the  $GF(2^m)$  sense) to the adder  $z$  if the  $i^{\text{th}}$  element of the representation  $c_f(x)$  342 is 1. STEP 630 increments counter  $i$  by 1. DECISION 635 checks to determine whether the counter  $i$  (as present before STEP 630) is less than minimal polynomial degree  $k_j$  354. STEP 640 assigns the contents of the adder  $z$  (as computed in STEP 625) to the syndrome  $s_j$  222.

FIG. 7 represents one example of logic 700 for producing syndromes  $s$  225 (FIG. 2). In one example, syndrome computer 240 (FIG. 2) employs (e.g., performs) logic 700. A further example employs a starting power  $L=0$ . From Equation (2), a result of setting  $L=0$  is that a syndrome  $s_j$  222 corresponds to a generator polynomial root  $\alpha^j$  212. So, in one example, an odd-numbered syndrome  $s_{2j-1}$  222 corresponds to an odd-powered generator polynomial root  $\alpha^{2j-1}$  212. Further, the odd-numbered syndrome  $s_{2j-1}$  222 has an odd-numbered representation  $c_{2j-1}(x)$  342. Similarly, an even-numbered syndrome  $s_{2j}$  222 corresponds to an even-powered generator polynomial root  $\alpha^{2j}$  212. In addition, the even-numbered syndrome  $s_{2j}$  222 has an even-numbered representation  $c_{2j}(x)$  342.

In one example, any even number  $e$  can be expressed as a product of an odd integer  $b$  and powers of two, such as by employment of the following exemplary Equation (9).

$$b = e/2^h \quad (9)$$

In Equation (9), the power  $h$  is a non-negative integer.

From Equations (1) and (9), those skilled in the art will appreciate that an even-numbered syndrome  $s_e$  222 can be computed by employing an odd-numbered representation  $c_b(x)$  342 and one or more instances of conversion mask  $d_e(x)$  322, such as by employment of the following exemplary Equation (10).

$$s_e = \sum_{i=0}^{k_j-1} c_i^{(b)} d_i^{(e)} \quad (10)$$

In Equation (10),  $c_i^{(b)}$  comprises the  $i^{\text{th}}$  member of the odd-numbered representation  $c_b(x)$  342. In addition,  $d_i^{(e)}$  comprises the  $i^{\text{th}}$  member of the one or more instances of



5 Equation (2), as will be appreciated by those skilled in the art.

10 odd, STEP 720 processes received vector  $r(x)$  122 using initialization 118 and  
minimal polynomial  $p_i(x)$  352, to produce an odd-numbered instance of  
representation  $c_j(x)$  342. Since  $L=0$ , in an example, the minimal polynomial  $p_i(x)$  352  
corresponds to an odd-powered root of generator polynomial  $g(x)$  114. STEP 725  
stores the odd-numbered instance of representation  $c_j(x)$  342, for example, in  
15 random access memory ("RAM") 726.

20 counter  $j$  (as present after STEP 735) is less than or equal to the number,  $y$ , of  
necessary roots. In one example,  $y$  comprises  $2t$  values.

Further referring to FIG. 7, in the event that DECISION 715 determines counter  $j$  is even, STEP 750 factors the counter  $j$  into an odd number  $b$  and powers of two. One example of such factorization can employ Equation (9). STEP 755, in

one example, fetches from RAM 726 the odd-numbered instance of representation  $c_j(x)$  342 corresponding to the odd-number  $b$ . STEP 760, in one example, employs logic 600 (FIG. 6) to produce an even-numbered instance of syndrome  $s_j$  222 from an odd-numbered instance of representation  $c_j(x)$  342 (determined in STEP 755).

5 Referring again to FIG. 7, logic 700, in one example, serves to produce syndromes  $\underline{s}$  225 (FIG. 2) based on sequential processing of the syndromes  $\underline{s}$  225. In another example, logic 700 first computes instances of odd-numbered representation  $c_{2j-1}(x)$  342. Subsequently, in this example, logic 700 computes syndromes  $\underline{s}$  225, in any order, from the odd-numbered instances of representation  $c_{2j-1}(x)$  342. In yet another example, logic 700 produces syndromes  $\underline{s}$  225 by computing syndromes  $s_j$  222 that correspond to an odd-numbered instance of representation  $c_b(x)$  342. With  $L=0$  and  $t=4$ , one example of logic 700 can determine (e.g., compute) syndromes  $s_1, s_2, s_4$ , and  $s_8$  once logic 700 has determined (e.g., computed) an odd-numbered instance of representation  $c_1(x)$  342, since syndromes  $s_1, s_2, s_4$ , and  $s_8$  correspond to the same odd-numbered instance of representation  $c_1(x)$  342. Similarly, one example of logic 700 can determine syndromes  $s_3$  and  $s_6$  once logic 700 has determined an odd-numbered instance of representation  $c_3(x)$  342, since syndromes  $s_3$  and  $s_6$  correspond to the same odd-numbered instance of representation  $c_3(x)$  342, as will be appreciated by those skilled in the art.

20 The flow diagrams depicted herein are just exemplary. There may be many variations to these diagrams or the steps (or operations) described therein without departing from the spirit of the invention. For instance, the steps may be performed in a differing order, or steps may be added, deleted or modified. All these variations are considered a part of the claimed invention.

5

[illegible]